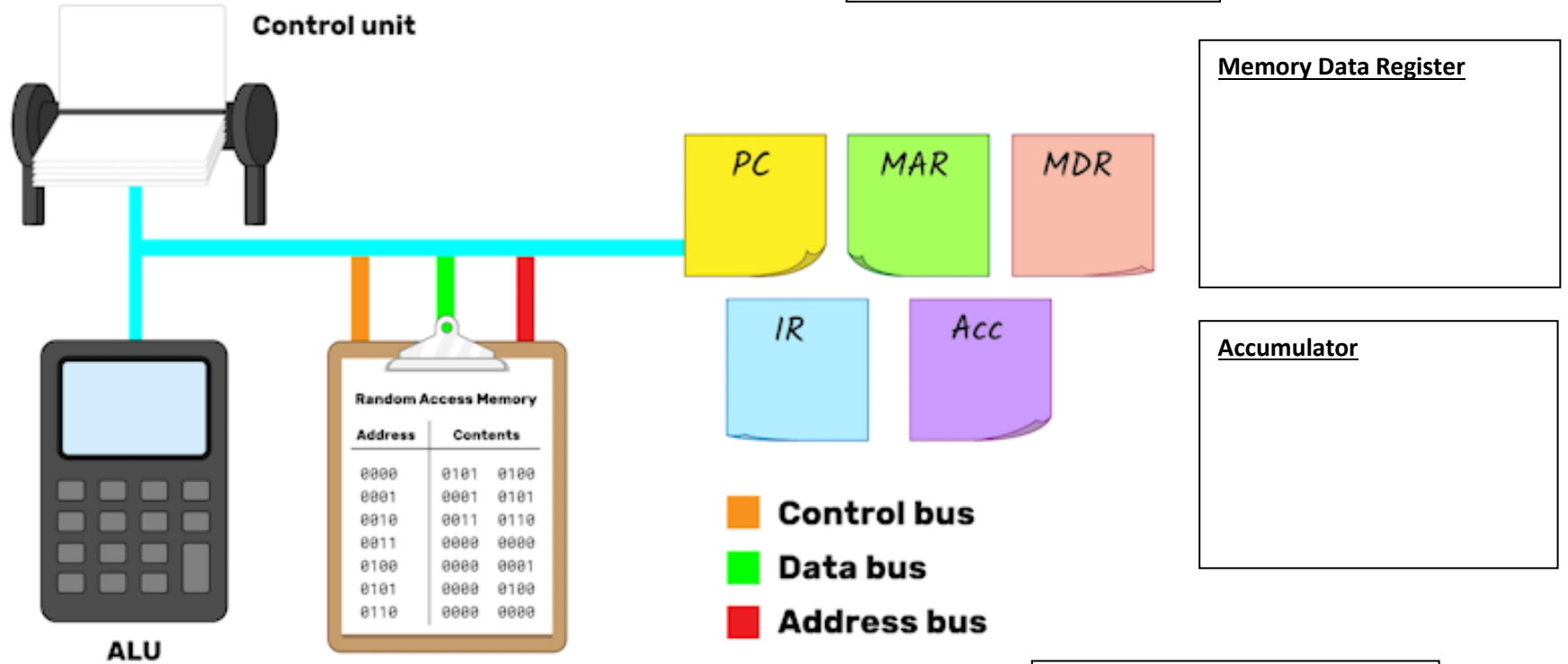


How registers are used in the Fetch-decode-execute

Program counter

Memory Address Register



Excellence: Control/data and address Buses:

Blank area for notes related to the bus systems.

Blank area for notes related to the registers.